

Exact separation of forbidden-set cuts associated with redundant parity checks of binary linear codes

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Abstract—In recent years, several integer programming (IP) approaches were developed for maximum-likelihood decoding and minimum distance computation for binary linear codes. Two aspects in particular have been demonstrated to improve the performance of IP solvers as well as adaptive linear programming decoders: the dynamic generation of forbidden-set (FS) inequalities, a family of valid cutting planes, and the utilization of so-called redundant parity-checks (RPCs). However, to date, it had remained unclear how to solve the exact RPC separation problem (i.e., to determine whether or not there exists any violated FS inequality w.r.t. any known or unknown parity-check). In this note, we prove NP-hardness of this problem. Moreover, we formulate an IP model that combines the search for most violated FS cuts with the generation of RPCs, and report on computational experiments. Empirically, for various instances of the minimum distance problem, it turns out that while utilizing the exact separation IP does not appear to provide a computational advantage, it can apparently be avoided altogether by combining heuristics to generate RPC-based cuts.

Index Terms—computational complexity, linear codes, binary codes, integer linear programming, Hamming weight

I. INTRODUCTION & PRELIMINARIES

THE well-known minimum distance computation problem for binary linear codes, defined by a parity-check matrix $H \in \{0, 1\}^{m \times n}$, can be stated as

$$\begin{aligned} \min \quad & \|\mathbf{x}\|_0 \\ \text{s.t.} \quad & H\mathbf{x} = \mathbf{0} \pmod{2}, \mathbf{x} \neq \mathbf{0}, \mathbf{x} \in \{0, 1\}^n, \end{aligned} \quad (1)$$

where $\|\cdot\|_0$ denotes the Hamming weight (i.e., number of nonzeros). Similarly, the maximum-likelihood decoding (MLD) problem is obtained by replacing the objective function with $\gamma^\top \mathbf{x}$, where γ is a (known) vector of negative log-likelihoods (see, e.g., [1]). Both problem (1) and MLD are NP-hard, see [2] and [3], respectively. For simplicity, we will focus on (1) throughout, but emphasize that as we will mainly deal with issues pertaining to the feasible set, the results are also applicable in the MLD context.

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Problem (1) (and analogously, the MLD problem) can be rewritten in several ways (see, e.g., [4]–[8]); a fairly straightforward, and the most common generic approach (cf. [9]–[11]) is to reformulate (1) as the integer program

$$\begin{aligned} \min \quad & \mathbb{1}^\top \mathbf{x} \\ \text{s.t.} \quad & H\mathbf{x} - 2\mathbf{z} = \mathbf{0}, \mathbb{1}^\top \mathbf{x} \geq 1, \mathbf{x} \in \{0, 1\}^n, \mathbf{z} \in \mathbb{Z}_{\geq 0}^n, \end{aligned} \quad (2)$$

where $\mathbb{1}$ is the all-ones vector. Indeed, it is easily seen that the auxiliary nonnegative integer variables \mathbf{z} enforce the required (even) codeword parity in all rows of the equality constraints.

Modern IP solvers are based on effective combination of the branch-and-bound paradigm (creating subproblems by fixing some variables and pruning parts of the resulting search tree, e.g., if local objective bounds prevent further improvements in the respective subtree) and the use of (linear) cutting planes, i.e., inequalities that are valid for integral feasible points but may be violated by fractional solutions of the commonly employed linear programming (LP) relaxations. Polyhedral investigations of the codeword polytope (i.e., the convex hull of integer feasible points of (1)) in [7], [12]–[18] (and other works) have identified, in particular, the following class of valid inequalities to strengthen the LP relaxation and to improve solver performance when tackling problems like (2):

$$\sum_{j \in S} x_j - \sum_{j \in \text{supp}(\mathbf{h}) \setminus S} x_j \leq |S| - 1, \quad (3)$$

where $\emptyset \neq S \subset \text{supp}(\mathbf{h}) := \{j \in \{1, 2, \dots, n\} : h_j \neq 0\}$ is odd and \mathbf{h} is a parity-check. These inequalities became known as (odd) forbidden-set (FS) inequalities. It has been established that for any given parity-check, at most one of the associated FS inequalities can be violated at a time (by a given point) [19], and that if a violated one (called a *cut*) exists, it can be found in polynomial time; the state-of-the-art method for the latter is the sorting-based routine described in [20] (see also [21]).

Moreover, several papers discussing IP or adaptive LP approaches for (1) or MLD incorporate heuristics to find new redundant parity-checks (RPCs)—i.e., some linear combinations (over \mathbb{F}_2 , i.e., in modulo-2 arithmetic) of rows of H —that hopefully yield violated FS inequalities, see, for instance, [19]–[22]. While some results exist on when such heuristics will succeed in finding a cut that is violated by a given (fractional) point (cf., e.g., [10], [19]), there is no general guarantee for this. Indeed, it has often been claimed that the problem of finding cut-inducing RPCs is intractable (see, e.g., [10], [19], [21]), but a formal proof of this statement appears to be missing so far.

In the following, we close this gap by providing an NP-hardness proof of the RPC separation problem. Moreover, we will introduce an IP model to solve it, i.e., to find a maximally violated FS cut among all possible FS inequalities w.r.t. all parity-checks, known or unknown (also allowing to conclude that all FS inequalities are satisfied, should this be the case), and assess its potential practical applicability in some computational experiments with problem (2).

II. EXACT RPC CUT GENERATION/SEPARATION

Clearly, if for a given $\mathbf{x}^* \in [0, 1]^n$, not all FS inequalities (3) are satisfied, there exists at least one parity-check \mathbf{h} and odd index set $S \subset \text{supp}(\mathbf{h})$ such that the constraint violation

$$-|S| + 1 + \sum_{j \in S} x_j^* - \sum_{j \in \text{supp}(\mathbf{h}) \setminus S} x_j^* > 0.$$

Thus, the *RPC separation problem* can be stated as follows: *Given $\mathbf{x}^* \in [0, 1]^n$, find a valid parity-check that yields a maximally violated FS cut, or conclude that all FS inequalities are satisfied.* This task can be formulated as the optimization problem

$$\begin{aligned} \max \quad & -|S| + 1 + \sum_{j \in S} x_j^* - \sum_{j \in \text{supp}(\mathbf{h}) \setminus S} x_j^* \quad (4) \\ \text{s.t.} \quad & \mathbf{h} \text{ is a parity-check,} \\ & \emptyset \neq S \subseteq \text{supp}(\mathbf{h}), |S| \text{ odd;} \end{aligned}$$

clearly, the optimal objective value is strictly larger than zero if and only if a cut-inducing parity-check \mathbf{h} exists. The objective is the violation of the respective FS inequality associated with \mathbf{h} and an odd subset S of its support at the given point \mathbf{x}^* , and the optimization problem searches for the largest such violation among all valid parity-checks.

With a few observations, we can turn the above abstract optimization problem into an equivalent concrete IP: First, recall that parity-checks correspond to dual codewords, cf. [19]; thus, we can express the first constraint in terms of a generator matrix \mathbf{G} of the code (which can be obtained as a basis matrix for the \mathbb{F}_2 -nullspace of the given parity-check matrix \mathbf{H} by Gaussian elimination) as $\mathbf{G}^\top \mathbf{h} = \mathbf{0} \pmod{2}$ with $\mathbf{h} \in \{0, 1\}^n$. To model the odd subsets S of $\text{supp}(\mathbf{h})$, we introduce binary variables $\mathbf{s} \leq \mathbf{h}$ with the constraint that $\mathbf{1}^\top \mathbf{s}$ is odd, i.e., $\mathbf{1}^\top \mathbf{s} = 1 \pmod{2}$. The condition $S \neq \emptyset$ is then reflected as $\mathbf{1}^\top \mathbf{s} \geq 1$, and is automatically ensured by the previous odd-sum constraint. The objective of (4) can be rewritten as

$$\begin{aligned} & -|S| + 1 - \sum_{j \in \text{supp}(\mathbf{h})} x_j^* + 2 \sum_{j \in S} x_j^* \\ & = -\mathbf{1}^\top \mathbf{s} + 1 - (\mathbf{x}^*)^\top \mathbf{h} + 2(\mathbf{x}^*)^\top (\mathbf{s}) \\ & = (2\mathbf{x}^* - \mathbf{1})^\top \mathbf{s} - (\mathbf{x}^*)^\top \mathbf{h} + 1. \end{aligned}$$

Assuming w.l.o.g. that $\text{rank}(\mathbf{H}) = m < n$ (so $\mathbf{G} \in \{0, 1\}^{n \times (n-m)}$ with $\text{rank } n - m$), with auxiliary integer variables $\mathbf{z}^h \in \mathbb{Z}_{\geq 0}^{n-m}$ and $z^s \in \mathbb{Z}_{\geq 0}$ to linearize the modulo-2

equations, we thus arrive at the *RPC separation IP* (equivalent to (4))

$$\begin{aligned} \max \quad & (2\mathbf{x}^* - \mathbf{1})^\top \mathbf{s} - (\mathbf{x}^*)^\top \mathbf{h} + 1 \quad (5) \\ \text{s.t.} \quad & \mathbf{G}^\top \mathbf{h} - 2\mathbf{z}^h = \mathbf{0} \\ & \mathbf{1}^\top \mathbf{s} - 2z^s = 1 \\ & \mathbf{s} \leq \mathbf{h} \\ & \mathbf{h}, \mathbf{s} \in \{0, 1\}^n, \mathbf{z}^h \in \mathbb{Z}_{\geq 0}^{n-m}, z^s \in \mathbb{Z}_{\geq 0}. \end{aligned}$$

(An alternative formulation that does not require knowledge, or construction, of the generator matrix \mathbf{G} can be obtained by replacing the first equality constraint by $\mathbf{h} = \mathbf{H}^\top \mathbf{w} - 2\mathbf{z}^h$ with $\mathbf{w} \in \{0, 1\}^m$ and $\mathbf{z}^h \in \mathbb{Z}_{\geq 0}^m$; however, this variant has m additional binary variables and m more integer variables.)

Our following result establishes that, in general, exact RPC separation is computationally challenging:

Theorem 1. *The RPC separation problem is NP-hard.*

Proof. The NP-hardness is due to the fact that the separation problem contains as a special case the task of finding the girth of a binary matroid (i.e., the minimum distance problem (1)), which is well-known to be NP-hard [2]: Let $\mathbf{A} \in \{0, 1\}^{m \times n}$ be an arbitrary instance of the binary matroid girth problem; w.l.o.g., we may assume $\text{rank}(\mathbf{A}) = m < n$ (over \mathbb{F}_2). With $\mathbf{x}^* := (1/2)\mathbf{1}$ and $\mathbf{G}^\top := \mathbf{A}$, the corresponding instance of the RPC separation problem (in IP form (5)) collapses to

$$\begin{aligned} \max_{\mathbf{s}, \mathbf{h} \in \{0, 1\}^n} \quad & -\frac{1}{2} \mathbf{1}^\top \mathbf{h} + 1 \\ \text{s.t.} \quad & \mathbf{A}\mathbf{h} = \mathbf{0} \pmod{2}, \mathbf{s} \leq \mathbf{h}, \mathbf{1}^\top \mathbf{s} = 1 \pmod{2} \\ \Leftrightarrow \quad & 1 - \frac{1}{2} \min_{\mathbf{h} \in \{0, 1\}^n} \{ \mathbf{1}^\top \mathbf{h} : \mathbf{A}\mathbf{h} = \mathbf{0} \pmod{2}, \mathbf{h} \neq \mathbf{0} \}. \end{aligned}$$

The latter minimization problem is precisely the girth problem for the instance given by \mathbf{A} , so unless $\mathbf{P} = \mathbf{NP}$, there cannot exist a polynomial-time algorithm for the RPC separation problem. \square

Remark 2. *Note that, although the above proof shows hardness of a separation problem for a large class of valid inequalities for the minimum distance problem (1) (and others) by exhibiting that very same problem as a special case, the result is not implied by the general so-called “equivalence of optimization and separation” (cf. [23]). Indeed, even if all FS inequalities for all parity-checks (given and redundant ones) were added to the IP model (2), its LP relaxation is not guaranteed to yield an integral (binary) optimum point [24].*

Moreover, using results from [25], NP-hardness of the RPC separation problem can be seen to persist even if \mathbf{A} is restricted to be the parity-check matrix of an LDPC code.

Remark 3. *The problem obtained by minimizing the RPC separation IP’s objective (instead of maximizing it) is, in fact, also NP-hard, as it contains the well-known max-cut problem as a special case (similar to the problem of finding a cycle of maximum weight in a binary matroid, cf. [13]); we omit the proof for brevity. The fact that both maximizing and minimizing the objective function over the feasible set of the RPC separation problem are NP-hard indicates that the hardness is inherent in the combinatorial constraints.*

(Similarly, the paper [26] gave a hardness-of-maximization result complementing the intractability of the minimization problem (1) shown later in [2].)

Naturally, given its inherent intractability as established by Theorem 1, solving the RPC separation IP (5) will be more challenging than running the heuristic schemes from, e.g., [20], [21]. Ideally, the additional computational effort to gain stronger cuts (and corresponding “optimal” new parity-checks) could lead to a sufficient benefit in terms of search space reduction (particularly, LP relaxation tightening) to compensate for the increased runtime overhead that can be expected. It is also worth pointing out that the IP (5) may also be used in a heuristic fashion: any feasible solution with positive objective value yields a violated FS inequality, so the solving process may be terminated early as soon as the current objective value turns positive, still providing a cut (although not necessarily a most violated one).

III. COMPUTATIONAL EXPERIMENTS

We now turn to some numerical experiments, based on the minimum distance IP (2), to assess the influence of separating FS inequalities and redundant parity-checks in different ways. We employ the open-source MIP solver SCIP [27] (with the LP solver SoPlex that comes with it) and use a variety of test instances from the Channel Code Database [28]. Our test set consists of 27 parity-check matrices, of which 5 belong to array codes, 3 to BCH/Hamming codes, 6 to LDPC codes, 2 to polar codes, one is the Tanner(3,5) code, 5 belong to WiMax and 3 to WiMax-like codes, and 2 to WRAN codes. The average matrix size is about 106×372 , ranging from 8 to 588 rows (parity-checks) and from 32 to 1344 variables. For brevity, we forego instance-specific details.

All experiments were carried out in single-thread mode on a Linux machine with Intel Core i7-7700T CPUs (2.9 GHz, 8 MB cache) and 16 GB memory, using SCIP 6.0.2. We set a time limit of 1 hour (3600 s) per instance.

The state-of-the-art separation routine from [20] for FS inequalities associated with *given* parity checks is, in fact, implemented in SCIP already (as part of the XOR constraint handler). We extended the implementation by the best-known RPC cut generation heuristics from [20] and from [21], respectively, as well as by an exact separation routine based on the RPC separation IP (5).

First, we used SCIP as a black-box, applied to the model (2); by default, SCIP does not separate FS inequalities. Then, with separation activated in the root node only, in every node, or on every 5-th level of the branch and bound tree, respectively, we tested the following solver variations:

- 1) exact separation of FS inequalities for existing parity-checks according to [20] (“ZS”, for short),
- 2) ZS, followed by exact RPC cut separation via (5), if ZS found no violated FS inequality (“ZSe”),
- 3) ZS, followed (if unsuccessful) by the RPC cut heuristic from [20], followed (if also unsuccessful) by the exact separation routine (“ZS+e”),
- 4) ZS, followed (if unsuccessful) by the RPC cut heuristic from [20], then (if still unsuccessful) by that from [21],

Table I
OVERVIEW OF EXPERIMENTAL RESULTS.

solver variant	gap (%)	# solved	nodes	time (s)
SCIP default	15.92	21	74464.3	112.9
ZS (sepa. root only)	12.37	21	81829.1	116.9
ZS (sepa. always)	1.45	26	4394.1	84.1
ZS (sepa. freq. 5)	1.28	26	14518.6	70.9
ZSe (sepa. root only)	12.37	21	81833.0	117.1
ZSe (sepa. always)	1526.64	6	145.2	1562.4
ZSe (sepa. freq. 5)	557.50	10	836.8	837.7
ZS+e (sepa. root only)	12.37	21	81816.9	117.1
ZS+e (sepa. always)	12.63	19	1517.0	468.6
ZS+e (sepa. freq. 5)	12.25	21	7751.2	293.4
ZS++e (sepa. root only)	17.60	21	150529.4	182.8
ZS++e (sepa. always)	5.43	24	2564.8	128.5
ZS++e (sepa. freq. 5)	7.76	23	9256.5	108.9

and finally (if still unsuccessful) by the exact separation routine (“ZS++e”).

We leave all of SCIP’s many other solver settings (w.r.t. general-purpose heuristics, cutting planes, branching rules etc.) at their respective default values. The solver also automatically takes care of cutting plane management, i.e., we neither force inequalities into the LP nor do we remove them by hand, but let the solver decide how to handle and store cuts. (Nevertheless, FS inequalities are given the highest separation priority, so that the solver first tries to find cuts of this type.)

Table I presents average results over all instances, namely the arithmetic mean optimality gap ((upper bound – lower bound)/(lower bound)·100%), the number of instances (out of 27) that were solved to optimality within the given time limit, and the average number of processed branch-and-bound (search tree) nodes as well as the average running times (in seconds) of the different solver variants. For nodes and times, we state shifted geometric mean values with shifts 100 and 10, respectively, to mitigate the influence of easy instances.

First of all, the overview in Table I clearly confirms previous studies: adding FS cuts during the solving process can significantly help solve IPs like (2). While adding cuts only in the root node can be seen to (adaptively) strengthen the original LP relaxation, the effect becomes much more prominent when parity-checks are also used to separate cuts in deeper levels of the search tree. Comparing the “SCIP default” results with, in particular, the “ZS” results, using (exact) separation of FS cuts based on the *known*, original parity-checks allows for more instances to be solved (thus also yielding a smaller average optimality gap) in significantly shorter time.

The results also show that more is not necessarily better: adding cuts at every search node does lead to the smallest average number of nodes that are explored before certifying optimality (or reaching the time limit), but apparently also significantly increases the time needed to solve the many (LP) subproblems during the branch-and-bound process. Separating FS cuts only at every 5-th level of the search tree typically yields results of comparable quality in a shorter average time, despite requiring more nodes to be explored.

The results incorporating the generation of redundant parity-checks in order to find further violated FS inequalities are somewhat ambiguous. Recall that we only ever used RPC-based schemes if the basic FS cut separation failed. Thus, the longer runtimes and less impressive gains in solution quality of the variants “ZSe”, “ZS+e” and “ZS++e” compared to “ZS” indicate that, on average, it does not pay off to search for FS cuts by exploring RPCs if the originally given parity-checks do not already yield such cuts. Nevertheless, it is important to emphasize that this is an empirical average statement only – indeed, e.g., for the Tanner(3,5) instance, the fastest “ZS” version (in this case, the one separating FS cuts in all nodes) solved the minimum distance problem in about 2029 s, whereas the “ZS++e” variant (also with separation in every node) only took roughly 1417 s. Thus, our results do not directly contradict earlier claims that RPC cuts help, although their benefit does appear to be less pronounced when integrating the corresponding separation schemes into a full MIP framework rather than more basic branch-and-cut or adaptive LP decoding procedures that had been explored before. (Recall also that [19] already noted that the relative improvement provided by RPC cuts decreases as the number of variables increases, which might have played a role here as well.)

Finally, the results for “ZSe” and “ZS+e” show that, unfortunately (though not unexpectedly, given Theorem 1), exact RPC cut separation does not pay off, as the IP separation subproblems are too hard in practice to be routinely solved within a branch-and-cut framework for the original problem. On the positive side, we note that in the variant “ZS++e”, i.e., where *exact* RPC separation would only be used if both state-of-the-art RPC cut *heuristics* failed, the separation IP (5) was actually *never* called at all. Thus, although one cannot be sure that the most violated cut was used, some violated cut was always found without having to resort to exact separation. Overall, however, the best results were still achieved by variant “ZS” that did not use RPC cuts at all.

IV. CONCLUDING REMARKS

In this note, we formally proved computational intractability of the RPC cut separation problem for binary linear codes. Moreover, our numerical experiments demonstrated that an associated IP formulation is indeed too hard to solve in practice to become useful during branch-and-cut methods. While our empirical results further showed that by combining the existing state-of-the-art RPC separation heuristics, the exact problem never needed to be resorted to, they also suggested that, on average, RPC cuts do not help improve the solution process of a current powerful MIP solver compared to using only FS cuts associated with the initial, known parity-checks.

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